

Electronic Acknowledgement Receipt

EFS ID:	1999317
Application Number:	10802199
International Application Number:	
Confirmation Number:	3761
Title of Invention:	Can microcontroller that permits concurrent access to different segments of a common memory by both the processor core and the DMA engine thereof
First Named Inventor/Applicant Name:	Ka Leung Ling
Customer Number:	24737
Filer:	Kenneth DeWayne Springer.
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Application Type:	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$ 620
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The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Extension of Time	NXP0018_PET.pdf	35012	no	1
			92596e4e50590ece0e79f6d3f34c2d2c2bf23675		
Warnings:					
Information:					
2	Notice of Appeal Filed	NXP0018_Notice_Of_Appeal.pdf	32680	no	1
			4712f0971dadd2c9b908edc5a7cf221870edf42a		
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8379	no	2
			93fa65d0da138935fecf18b7d5d76eba3a4e363a		
Warnings:					
Information:					
Total Files Size (in bytes):			76071		
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